THE COMMISSIONER OF PATENTS AND TRADEMARKS WASHINGTON, D.C. 20231

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Transmitted herewith for informal filing is the Utility patent application of:

INVENTOR:	DA
INVENTOR.	Masatoshi HASEGAWA, Shuichi MIYAOKA, Hiroshi AKASAKI
	and Masahiro KATAYAMA

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE,

METHOD OF TESTING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Enclosed are:

- \boxtimes 11 sheets of \square formal \square informal drawings.
- ☐ An Assignment of the invention to
- ☐ A preliminary amendment.
- ☑ A Notice of Priority claiming the priority date of February 1, 2001
- ☑ An Unexecuted Declaration

☐ The filing fee is calculated as shown below:

For	Numbei	R FILED	Number Extra	Rate	CALCULATIONS
TOTAL CLAIMS	12	-20		x\$18 =	
INDEPENDENT CLAIMS		-3		x\$84 =	
MULTIPLE DEPENDENT CLAIM(s) (if applicable) +\$280 =					
			BASIC FEE		740.00
			TOTAL OF ABOVE		740.00
REDUCTION BY ½ FOR FILING BY SMALL ENTITY (note 37 C F.R. §§ 1.9, 1.27, 1.28). IF APPLICABLE, VERIFIED STATEMENT MUST BE ATTACHED					
				TOTAL	740.00

Please charge my Deposit Account Number	in the amount of	to cover the filing and assignment recordation
fees. A duplicate copy of this paper is enclosed.		

- A check in the amount of \$ 740.00 to cover the filing fee is enclosed.
- The Commissioner is hereby authorized to charge any additional fees associated with this communication, including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit any overpayment to **Deposit Account Number 08-1480**..

Respectfully submitted,

Stanley P. Fisher
Registration Number 24,344

JUAN CARLOS A. MARQUEZ Registration No. 34,072

REED SMITH LLP 3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 January 30, 2002 10/058787 10/058787 10/058787

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